

David J. Attard

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Technical Background

- Technical development experience in multi-technology products.
- Broad technical background in millimeter, microwave, RF, analog and digital systems.
- 17+ years industry experience as Microwave/RF, analog, and digital design/system engineer.

Work Experience

2001 to Present

Sr. Staff Engineer, Trimble, Sunnyvale, CA

- Lead Project Engineer. GPS Satellite Receiver Subsystem architecture definition.
- Next generation L-Band GPS receiver development
- GENESYS and PSPICE EDA
- RFIC, RF, Analog, and Phase Lock Loop design.
- EMI and FCC certification testing
- Coordination of program staffing plans.
- New Product Development (NPD)

1999 to 2001

Sr. Staff Engineer / Engineering Manager, Stellex Microwave, San Jose, CA

- Project Engineer. Subsystem front-end architecture definition.
- Millimeterwave Out Door Unit (ODU) product transceiver architecture and spec definition.
- 26 to 38 GHz receive and transmit module design.
- IF/RF transceiver CCA development. Analog and PLL design.
- Responsible for section-level staff management. Program staffing plans, training/mentoring of engineering staff.

1995 to 1999

Member of the Technical Staff, Advanced Micro Devices, Sunnyvale, CA

- Subsystem design. DSSS / TDD / GSM / WLAN radio architecture and spec definition.
- Design of 0.34u CMOS based Synthesizer IC. Integrated serial I/O, Prescaler, Counters, PFD, and Charge Pump. Used Verilog and H-Spice simulation tools.
- Design of Bipolar based integrated synthesizer. Pspice and Hspice simulation tools
- RFIC, radio, and subsystem reference design test and integration.

1993 to 1995

Sr. Staff Engineer, RF Engineer Manager 1, TRW / ESL Inc., Sunnyvale, CA

- Responsible for section-level staff management. Program staffing plans, training/mentoring of engineering staff.
- RF Subsystem technical lead. Generation and allocation of critical system design specifications.
- System front-end architecture definition, sensitivity, and dynamic range analysis.
- Project Manager. Responsible for the development of multi-channel wideband receiver.
- Design of multi-layer PC boards integrating digital, analog, and RF circuitry.

1986 to 1993

RF Research & Development Engineer, TRW / ESL Inc., Sunnyvale, CA

- HF and V/UHF Preselector, Receiver IF Stages. Synthesizer/PLL Design.
- Microcontroller Design. VME based receiver development.

1981 to 1986

Lab Technician, TRW / ESL Inc., Sunnyvale, CA

Education

- Bachelor of Science in Electrical Engineering, Santa Clara University; June 1990.
- Associate in Science in Electronics Technology, College of San Mateo; January 1982.

References

Available upon request.